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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/510,974	05/04/2005	Stefan Schandl	2002P05576WOUS	6165

7590 03/17/2008

Siemens Corporation
Intellectual Property Department
170 Wood Avenue South
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EXAMINER	
GOEL, DINESH K	
ART UNIT	PAPER NUMBER
4134	

MAIL DATE	DELIVERY MODE
03/17/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/510,974	SCHANDL, STEFAN	
	Examiner	Art Unit	
	DINESH GOEL	4134	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *04 May 2005*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *6-21* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *6-21* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *08 October 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date *10/8/2004*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Specification

1. This application does not contain figure 4 as referred on page 3. An appropriate correction is required.

Drawings

2. The drawings are objected to because Fig. 4 is missing. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. **Claim 6- 21** are rejected under 35 U.S.C. 102(b) as being anticipated by Kurdzo et al (U.S. Patent No. 6088365).

Referring to **claim 6**, Kurdzo et al teaches a signal processing unit (Abstract, Figure 1, "voice server") , comprising: a mechanism for digital signal processing (Column 4 Lines 21-22 "DSP array" and Lines 30-38, Figure 2, "30"; and a control mechanism (Column 4 Lines 22-23 "adaptation layer processor", Figure 2, "40"), wherein the mechanism for digital signal processing and the control mechanism are connected by a serial time multiplex connection (Column 4 Lines 38-43 "data bus" and "flag bus", and Figure 2, "74" and "76").

Referring to **claim 7**, Kurdzo et al further teaches the signal processing unit according to claim 6, wherein the signal processing unit is used in a telecommunication system (Column 1 Lines 57-62).

Referring to **claim 8**, Kurdzo et al further teaches the signal processing unit according to claim 6, further comprising a mechanism for storing data (Column 6 Lines 31-32 “internal memory”).

Referring to **claims 9**, Kurdzo et al further teaches the signal processing unit according to Claim 6, wherein the serial time multiplex connection is implemented as a PCM 30 system (Column 4 Lines 38-43 “data bus” and “flag bus”, and Figure 2, “74” and “76”). These are in the form of 8.192 Mbit/s PCM data buses (Column 4 Lines 41), each consisting of four 2.048 Mbit/s PCM data buses (Column 4 Lines 25-29). The term PCM 30 system inherently signifies a group of 32 PCM channels (30 being bearer channels), and is also a synonym for a 2.048 Mbit/s PCM data bus. Consequently, the use of a PCM 30 system instead of a combination of PCM 30 system data buses would not be considered inventive.

Referring to **claims 10**, Kurdzo et al further teaches the signal processing unit according to Claim 7, wherein the serial time multiplex connection is implemented as a PCM 30 system (Column 4 Lines 38-43 “data bus” and “flag bus”, and Figure 2, “74” and “76”). These are in the form of 8.192 Mbit/s PCM data buses (Column 4 Lines 41), each consisting of four 2.048 Mbit/s PCM data buses (Column 4 Lines 25-29). The term PCM 30 system inherently signifies a group of 32 PCM channels (30 being bearer channels), and is also a synonym for

a 2.048 Mbit/s PCM data bus. Consequently, the use of a PCM 30 system instead of a combination of PCM 30 system data buses would not be considered inventive.

Referring to **claims 11**, Kurdzo et al further teaches the signal processing unit according to Claim 8, wherein the serial time multiplex connection is implemented as a PCM 30 system (Column 4 Lines 38-43 “data bus” and “flag bus”, and Figure 2, “74” and “76”). These are in the form of 8.192 Mbit/s PCM data buses (Column 4 Lines 41), each consisting of four 2.048 Mbit/s PCM data buses (Column 4 Lines 25-29). The term PCM 30 system inherently signifies a group of 32 PCM channels (30 being bearer channels), and is also a synonym for a 2.048 Mbit/s PCM data bus. Consequently, the use of a PCM 30 system instead of a combination of PCM 30 system data buses would not be considered inventive.

Referring to **claims 12,** the signal processing unit according to Claim 6, wherein the mechanism for digital signal processing is a digital signal processor and/or a mechanism for echo suppression. This is further taught by Kurdzo et al (Abstract; Column 6 Line 13 “DSP”; Column 8 Lines 10-13; and Figure 4a, “268”).

Referring to **claims 13,** the signal processing unit according to Claim 7, wherein the mechanism for digital signal processing is a digital signal processor and/or a

mechanism for echo suppression. This is further taught by Kurdzo et al (Abstract; Column 6 Line 13 “DSP”; Column 8 Lines 10-13; and Figure 4a, “268”).

Referring to **claims 14,** the signal processing unit according to Claim 8, wherein the mechanism for digital signal processing is a digital signal processor and/or a mechanism for echo suppression. This is further taught by Kurdzo et al (Abstract; Column 6 Line 13 “DSP”; Column 8 Lines 10-13; and Figure 4a, “268”).

Referring to **claims 15,** the signal processing unit according to Claim 9, wherein the mechanism for digital signal processing is a digital signal processor and/or a mechanism for echo suppression. This is further taught by Kurdzo et al (Abstract; Column 6 Line 13 “DSP”; Column 8 Lines 10-13; and Figure 4a, “268”).

Referring to claims **16**, the signal processing unit according to Claim 6, wherein the signal processing unit is implemented as a separate module. This is further taught by Kurdzo et al (Column 3 Lines 66-67 ”controller card”; Column 4 Lines 9-11; and Figure 1, “18a-d”).

Referring to claims **17**, the signal processing unit according to Claim 7, wherein the signal processing unit is implemented as a separate module. This is further taught by Kurdzo et al (Column 3 Lines 66-67 ”controller card”; Column 4 Lines 9-11; and Figure 1, “18a-d”).

Referring to claims **18**, the signal processing unit according to Claim 8, wherein the signal processing unit is implemented as a separate module. This is further taught by Kurdzo et al (Column 3 Lines 66-67 ”controller card”; Column 4 Lines 9-11; and Figure 1, “18a-d”).

Referring to claims **19**, the signal processing unit according to Claim 9, wherein the signal processing unit is implemented as a separate module. This is further taught by Kurdzo et al (Column 3 Lines 66-67 ”controller card”; Column 4 Lines 9-11; and Figure 1, “18a-d).

Referring to claims **20**, the signal processing unit according to Claim 12, wherein the signal processing unit is implemented as a separate module. This is further taught by Kurdzo et al (Column 3 Lines 66-67 ”controller card”; Column 4 Lines 9-11; and Figure 1, “18a-d”).

Referring to **claim 21**, Kurdzo et al further teaches the signal processing unit according to Claim 16, wherein the signal processing unit is implemented as a separate module of an exchange of a digital switching system. This is further taught by Kurdzo et al (Abstract; Column 3 Lines 56-61; and Figure 1).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DINESH GOEL whose telephone number is (571)270-5201. The examiner can normally be reached on Monday-Friday 8:00 AM-5:00 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lun-Yi Lao can be reached on 571-272-7671. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Dinesh Goel/
Examiner, Art Unit 4134

/LUN-YI LAO/

Supervisory Patent Examiner, Art Unit 4134